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Substitute for form 1449A/PTO			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		<i>Complete If Known</i>	
		Application Number	10/068,326
		Filing Date	February 6, 2002
		First Named Inventor	Lai, Benny W H
		Art Unit	2117
		Examiner Name	Tabone Jr, John J.
Sheet	1	of	2
Attorney Docket No: 10004400-01			

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Examiner Initial *	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Filing Date If Appropriate
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EXAMINER /John J. Tabone Jr. / (10/30/2007)

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Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. † Applicant's unique citation designation number (optional) ‡ Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete If Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/068,326</td> </tr> <tr> <td>Filing Date</td> <td>February 6, 2002</td> </tr> <tr> <td>First Named Inventor</td> <td>Lai, Benny W H</td> </tr> <tr> <td>Art Unit</td> <td>2117</td> </tr> <tr> <td>Examiner Name</td> <td>Tabone Jr, John J.</td> </tr> </table>		Application Number	10/068,326	Filing Date	February 6, 2002	First Named Inventor	Lai, Benny W H	Art Unit	2117	Examiner Name	Tabone Jr, John J.
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		TAKESHITA, T. "A 622Mb/s Fully-Integrated Optical IC with a Wide Range Input", IEEE, International Solid-State Circuits Conference, (2002),	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Complete If Known

Application Number	10/993,894 ← incorrect App.
Filing Date	November 18, 2004
First Named Inventor	Lai, Benny
Art Unit	2117
Examiner Name	Tabone Jr., John J.

(Use as many sheets as necessary)

Sheet

1

of

2

Attorney Docket No: 10030807-05

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		TAKESHITA, T. "A 622Mb/s Fully-Integrated Optical IC with a Wide Range Input", IEEE, International Solid-State Circuits Conference, (2002),			
		SKLAR, B. "Digital Communications: Fundamentals and Applications", 2nd Ed., Upper Saddle River, NJ: Prentice Hall PTR, (2001),			
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		PEPELJUGOSKI, P. et al., "Improved Performance of 10 Gb/s Multimode Fiber Optic Links Using Equalization", Optical Fiber Communications Conference, OFC 2003, Vol 2., (March 23-28 2003), 472-474			

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